REMARKS

Claim 1, for example, calls for conversion logic. That conversion logic is to generate a modified virtual address from the virtual address if the virtual address-to-physical address translation for the virtual address does not exist in the storage. The page walk table unit then is configured to convert the modified virtual address into the corresponding physical address. Thus, the course of the translation through to the page walk table takes an intermediate step according to the claim, in which a conversion is made to a modified virtual address in those cases where the entry does not exist.

It is respectfully submitted that the cited reference does no such thing. In the material apparently being relied upon, in paragraphs 7 and 8 of the cited reference, it is indicated that there is page walking. *See* paragraph 7. Then in paragraph 8 it is stated that "[e]ach processing device 14 has a unique requester identifier that is concatenated to a virtual address to form a modified virtual address." In other words, the modified virtual address is presented in every single case. The modified address is used regardless of whether the virtual address-to-physical address translation for the virtual address does not exist in the storage.

Thus, there is no conversion logic that generates a modified virtual address "if a virtual address-to-physical address translation for the virtual address does not exist in the storage". Therefore reconsideration of the rejection of claim 1 is respectfully requested.

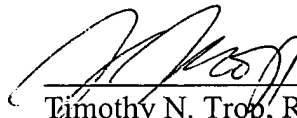On the same basis, reconsideration of the rejection of claim 11 would be appropriate.

Claim 17 states that you determine if the virtual address has a translation and, if not, "generating a modified virtual address from the virtual address". Thus the "if-not" step is not performed in the cited reference.

This is confirmed explicitly in paragraph 9 where it is stated that "[i]f there is a miss in the shared TLB 12, the virtual address is translated to a physical address through translation tables 16 (individually referenced as translation tables 16a-c in FIG.1) in the physical memory 18" [emphasis added]. Thus it is clear that the modified virtual address

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is not used in the case where there is a miss, but instead only the virtual address is used. For this additional reason, reconsideration would be appropriate.

Respectfully submitted,

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